

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	58	("4075691" "4156907" "4377843" "4399503" "4456957" "4488231"). PN. OR ("4604683").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/08/20 03:24
L9	1319	380/30.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/20 03:30
L10	690	713/185.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/20 03:30
L11	65	380/266.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/20 03:30

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S106	45769	(input near output) with (controller microcontroller cpu) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 17:54
S107	537	(input near output) near (part module) with (controller microcontroller cpu) with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 17:54
S108	45	(input near output) near (section part module) with (controller microcontroller cpu) with memory and bus with (computer host) with (input near output) near (section part module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 17:56
S109	85552	(input near output) near (section part module) with (controller microcontroller cpu) with memory and bus with (computer host) with (input near output) near (section part module) encryption	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 17:56
S110	2	(input near output) near (section part module) with (controller microcontroller cpu) with memory and bus with (computer host) with (input near output) near (section part module) and encryption	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 17:58
S111	1	(input near output) near (section part module) with (controller microcontroller cpu) with memory and encryption near circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/18 14:55
S112	15	(input near output) with (controller microcontroller cpu) with memory and encryption near circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 18:19
S113	2606	(input near output) with (controller microcontroller cpu) with memory and encryption	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 18:20
S114	594	(input near output) with (controller microcontroller cpu) with memory and (input near output) with bus and (dual near2 (memory ram rom))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 18:24

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S11 5	0	(input near output) with (controller microcontroller cpu) with memory and (input near output) with bus and (dual near2 (memory ram rom)) with (encryption cryptographic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/16 18:24
S14 4	1926	713/193.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/18 14:54
S14 5	4	((input near output) "i/o") near (section part module) with (controller microcontroller cpu) with memory and encryption near circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/18 14:56